

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Duane Wiens et al.

Confirmation No.:

Application No.: 09/853,738

Examiner: Unknown

Filing Date: May 10, 2001

Group Art Unit: 2123

Title: FAST PRIORITY DETERMINATION CIRCUIT WITH ROTATING PRIORITY

COMMISSIONER FOR PATENTS
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☒ under 37 CFR 1.97(b), or
(Within three months of filing national application; or date of entry of national application; or before mailing date of first office action on the merits; whichever occurs last)
- ☐ under 37 CFR 1.97 (c) together with either a:
☐ Statement under 37 CFR 1.97(e), or
☐ a \$180.00 fee under 37 CFR 1.17(p), or
(After the CFR 1.97 (b) time period, but before final action or notice of allowance, whichever occurs first)
- ☐ under 37 CFR 1.97 (d) together with a:
☐ Statement under 37 CFR 1.97(e), and
☐ a petition under 37 CFR 1.97(d)(2), and
☐ a \$180.00 petition fee set forth in 37 CFR 1.17(p).
(Filed after final action or notice of allowance, whichever occurs first, but before payment of the issue fee)

Please charge to Deposit Account 08-2025 the sum of \$0.00 . At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account 08-2025 pursuant to 37 CFR 1.25.

☒ Applicant(s) submit herewith Form PTO 1449 - Information Disclosure Citation together with copies, of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

☐ A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO Form 1449, as presently understood by the individuals(s) designated in 37 CFR 1.56 (c) most knowledgeable about the content is given on the attached sheet, or where a foreign language patent is cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action which indicates the degree of relevance found by the foreign office is listed on form PTO 1449 and is enclosed herewith.

It is requested that the information disclosed herein be made of record in this application.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231.

Date of Deposit: Oct. 31, 2001

Typed Name: Shelly White

Signature: Shelly White

Respectfully submitted,

Duane Wiens et al.

By

Jerry Mahurin
Jerry Mahurin

Attorney/Agent for Applicant(s)
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Date: Oct. 31, 2001

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FORM PTO-1449

ATTY. DOCKET NO.

10004753-1

APPLICATION NO.

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APPLICANT

Duane Wiens et al.

FILING DATE

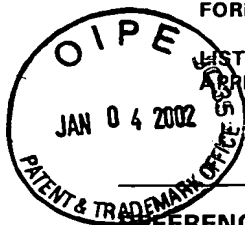
May 10, 2001

GROUP

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LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)



REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
	1A					
	1B					
	1C					
	1D					
	1E					
	1F					
	1G					
	1H					
	1I					
	1J					
	1K					

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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	TRANSLATION	
							YES	NO
	1L							
	1M							
	1N							
	1O							
	1P							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

1Q	De Blasi, "Computer Architecture," ISBN 0-201-41603-4 (Addison-Wesley, 1990), pp.273-291
1R	Stone, "High Performance Computer Architecture," ISBN 0-201-51377-3 (Addison-Wesley, 2d Ed. 1990), pp.29-39
1S	Tabak, "Advanced Microprocessors, " ISBN 0-07-062807-6 (McGraw-Hill, 1991) pp. 244-248

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PATENT APPLICATION

Sheet 2 of 2

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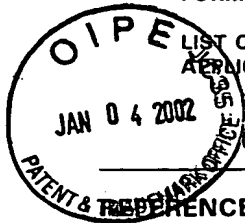
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	2M							
	2N							
	2O							
	2P							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

2Q	M. Papamarcos and J. Patel, "A Low Overhead Coherent Solution for Microprocessors with Private Cache Memories," in Proceedings of the 11th International Symposium on Computer Architecture, IEEE, New York (1984), pp. 348-354
2R	
2S	

EXAMINER

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